

WHAT IS CLAIMED IS:

1. An audio signal processing circuit comprising:
  - a thin film element formed over an insulating
  - 5 substrate;
  - a thin film resistor formed over the insulating
  - substrate; and
  - a chip capacitor mounted over the insulating substrate.
- 10 2. The audio signal processing circuit according to claim 1, wherein the audio signal processing circuit comprises an input circuit and the input circuit comprises thin film resistor and the chip capacitor.
- 15 3. The audio signal processing circuit according to claim 1, wherein the audio signal processing circuit comprises a feedback circuit and the feedback circuit comprises thin film resistor and the chip capacitor.
- 20 4. The audio signal processing circuit according to claim 1, wherein the audio signal processing circuit comprises a smoothing circuit and the smoothing circuit comprises thin film resistor and the chip capacitor.
- 25 5. The audio signal processing circuit according to

claim 1, wherein P-type impurities are doped in the thin film resistor.

6. The audio signal processing circuit according to  
5 claim 1, wherein the thin film resistor has a resistance value of 80 k $\Omega$  or more.

7. An electronic equipment comprising the audio signal processing circuit according to claim 1, wherein the  
10 electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

15 8. An audio signal processing circuit comprising:  
a thin film element formed over an insulating substrate;  
a thin film resistor formed over the insulating substrate; and  
20 a chip capacitor mounted over a flexible substrate connected to the insulating substrate.

9. The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit  
25 comprises an input circuit and the input circuit comprises

thin film resistor and the chip capacitor.

10. The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit  
5 comprises a feedback circuit and the feedback circuit comprises thin film resistor and the chip capacitor.

11. The audio signal processing circuit according to claim 8, wherein the audio signal processing circuit  
10 comprises a smoothing circuit and the smoothing circuit comprises thin film resistor and the chip capacitor.

12. The audio signal processing circuit according to claim 8, wherein P-type impurities are doped in the thin film  
15 resistor.

13. The audio signal processing circuit according to claim 8, wherein the thin film resistor has a resistance value  
of 80 k $\Omega$  or more.

20

14. An electronic equipment comprising the audio signal processing circuit according to claim 8, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted  
25 display, a game machine, a car navigation system, a personal

computer and a portable information terminal.

15. An audio signal processing circuit comprising:  
a thin film element formed over an insulating  
5 substrate;

a thin film resistor formed over the insulating  
substrate; and

a chip capacitor mounted over a printed circuit board  
electrically connected to the insulating substrate.

10

16. The audio signal processing circuit according to  
claim 15, wherein the audio signal processing circuit  
comprises an input circuit and the input circuit comprises  
thin film resistor and the chip capacitor.

15

17. The audio signal processing circuit according to  
claim 15, wherein the audio signal processing circuit  
comprises a feedback circuit and the feedback circuit  
comprises thin film resistor and the chip capacitor.

20

18. The audio signal processing circuit according to  
claim 15, wherein the audio signal processing circuit  
comprises a smoothing circuit and the smoothing circuit  
comprises thin film resistor and the chip capacitor.

25

19. The audio signal processing circuit according to claim 15, wherein P-type impurities are doped in the thin film resistor.

5           20. The audio signal processing circuit according to claim 15, wherein the thin film resistor has a resistance value of 80 k $\Omega$  or more.

          21. An electronic equipment comprising the audio signal  
10 processing circuit according to claim 15, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

15

22. A display device comprising:

a thin film element formed over an insulating substrate;

a thin film resistor formed over the insulating  
20 substrate; and

a chip capacitor mounted over the insulating substrate.

23. The display device according to claim 22, wherein the display device comprises an input circuit and the input  
25 circuit comprises thin film resistor and the chip capacitor.

24. The display device according to claim 22, wherein the display device comprises a feedback circuit and the feedback circuit comprises thin film resistor and the chip  
5 capacitor.

25. The display device according to claim 22, wherein the display device comprises a smoothing circuit and the smoothing circuit comprises thin film resistor and the chip  
10 capacitor.

26. The display device according to claim 22, wherein P-type impurities are doped in the thin film resistor.

15 27. The display device according to claim 22, wherein the thin film resistor has a resistance value of 80 k $\Omega$  or more.

28. An electronic equipment comprising the display  
20 device according to claim 22, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a portable information terminal.

25

29. A display device comprising:

a thin film element formed over an insulating substrate;

a thin film resistor formed over the insulating  
5 substrate; and

a chip capacitor mounted over a flexible substrate connected to the insulating substrate.

30. The display device according to claim 29, wherein  
10 the display device comprises an input circuit and the input circuit comprises thin film resistor and the chip capacitor.

31. The display device according to claim 29, wherein  
the display device comprises a feedback circuit and the  
15 feedback circuit comprises thin film resistor and the chip capacitor.

32. The display device according to claim 29, wherein  
the display device comprises a smoothing circuit and the  
20 smoothing circuit comprises thin film resistor and the chip capacitor.

33. The display device according to claim 29, wherein  
P-type impurities are doped in the thin film resistor.

34. The display device according to claim 29, wherein the thin film resistor has a resistance value of 80 k $\Omega$  or more.

5           35. An electronic equipment comprising the display device according to claim 29, wherein the electronic equipment is one selected from the group consisting of a video camera, a digital camera, a head mounted display, a game machine, a car navigation system, a personal computer and a  
10 portable information terminal.

36. A display device comprising:

          a thin film element formed over an insulating substrate;  
15           a thin film resistor formed over the insulating substrate; and  
          a chip capacitor mounted over a printed circuit board electrically connected to the insulating substrate.

20           37. The display device according to claim 36, wherein the display device comprises an input circuit and the input circuit comprises thin film resistor and the chip capacitor.

          38. The display device according to claim 36, wherein  
25 the display device comprises a feedback circuit and the



feedback circuit comprises thin film resistor and the chip capacitor.

39. The display device according to claim 36, wherein  
5 the display device comprises a smoothing circuit and the smoothing circuit comprises thin film resistor and the chip capacitor.

40. The display device according to claim 36, wherein  
10 P-type impurities are doped in the thin film resistor.

41. The display device according to claim 36, wherein  
the thin film resistor has a resistance value of 80 k $\Omega$  or more.  
15

42. An electronic equipment comprising the display  
device according to claim 36, wherein the electronic  
equipment is one selected from the group consisting of a video  
camera, a digital camera, a head mounted display, a game  
20 machine, a car navigation system, a personal computer and a portable information terminal.